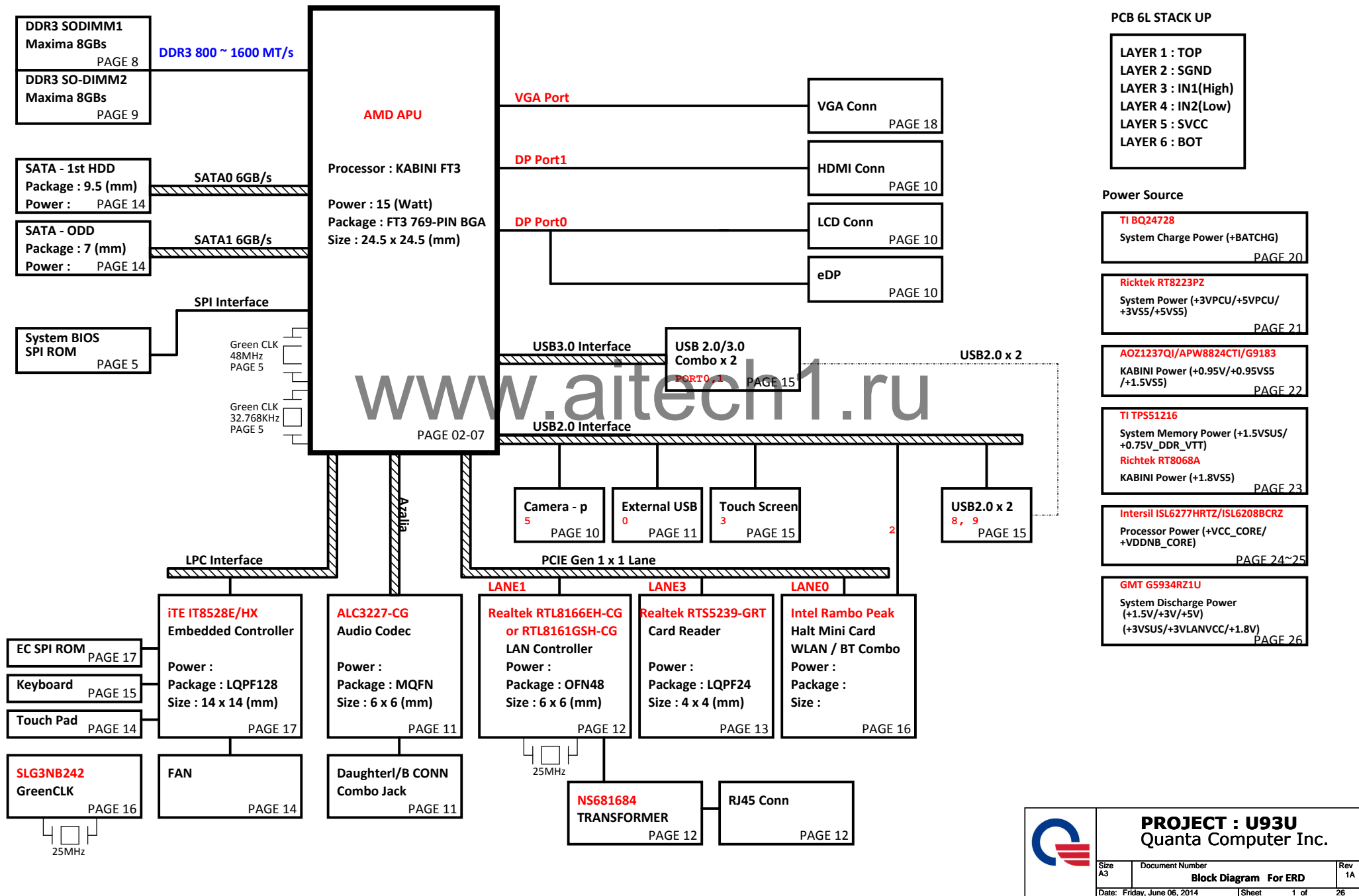
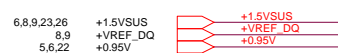
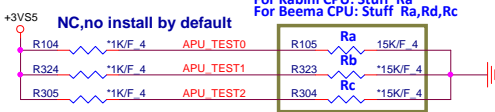
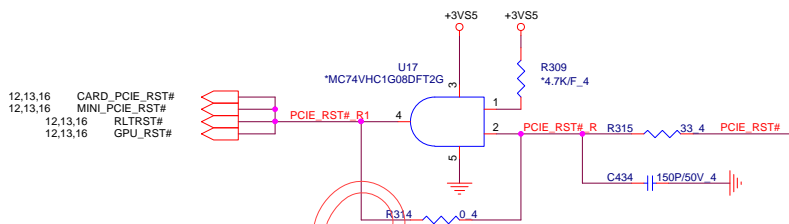


Lean G_AMD KABINI UMA (14"/15.6")

Ultra/Slim⁰¹

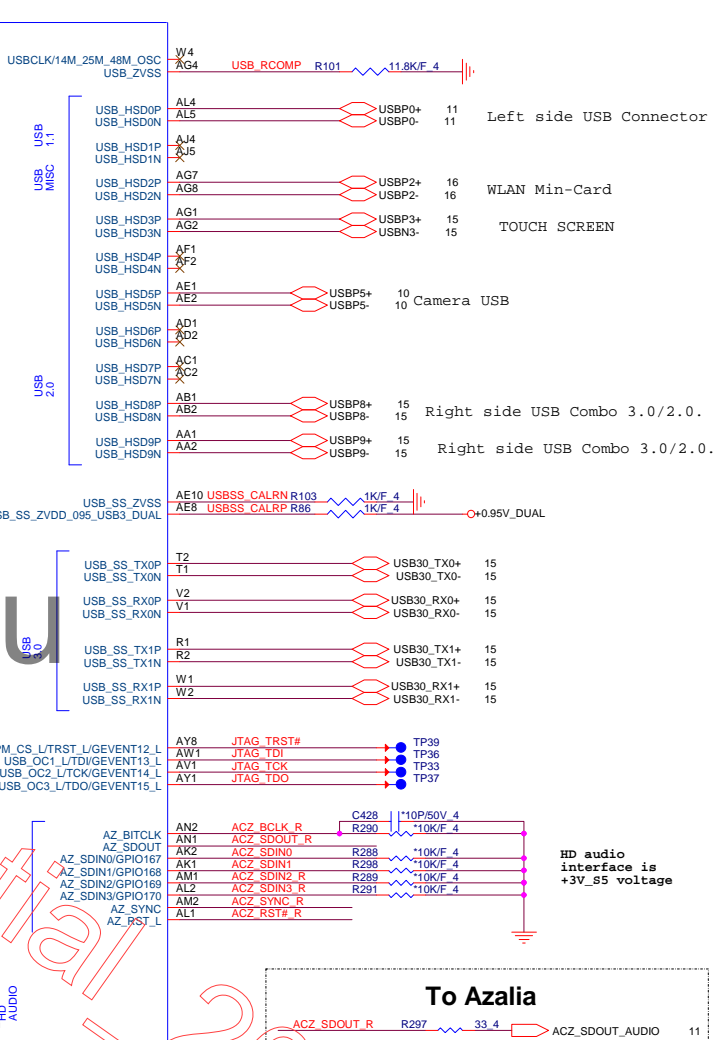
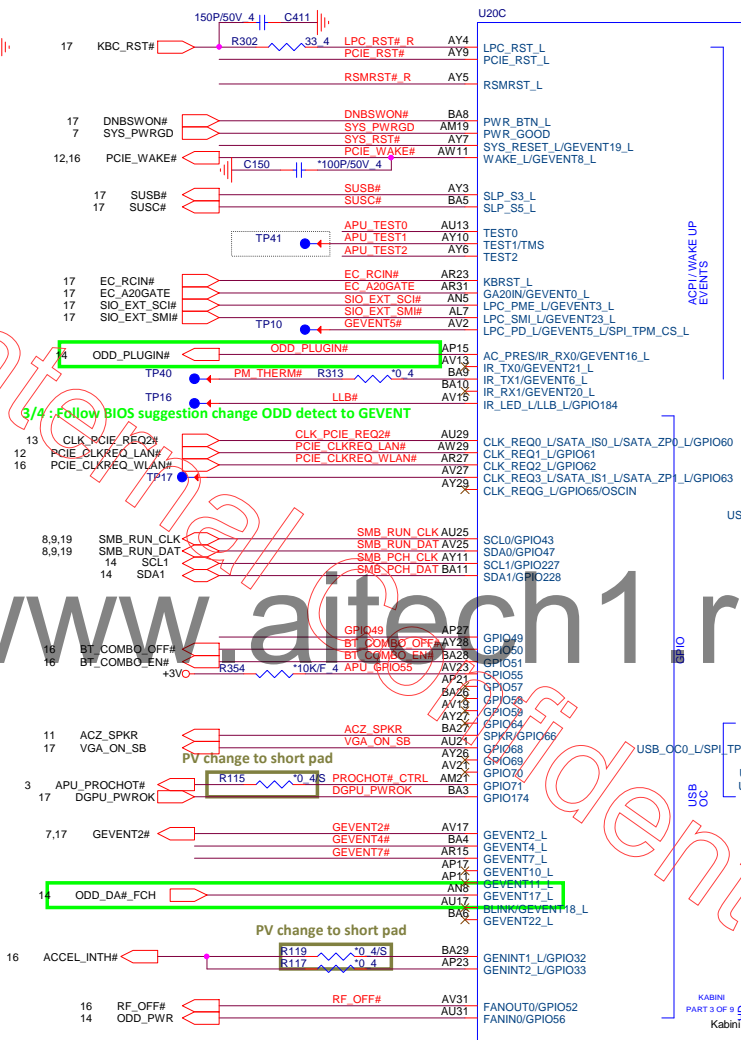
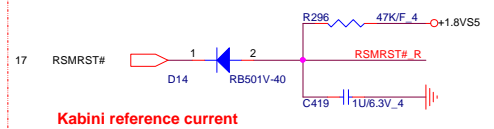
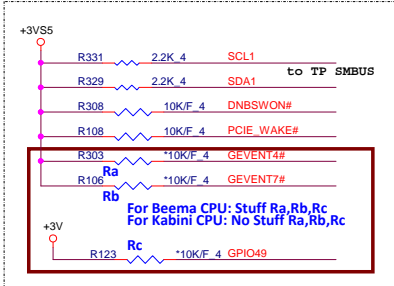
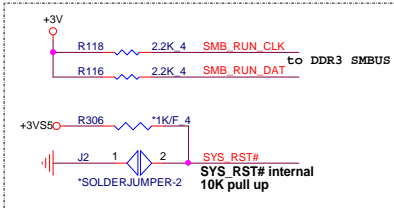




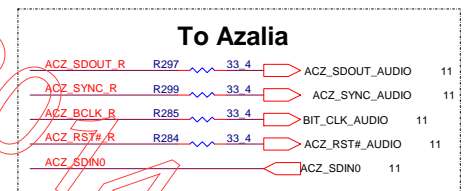


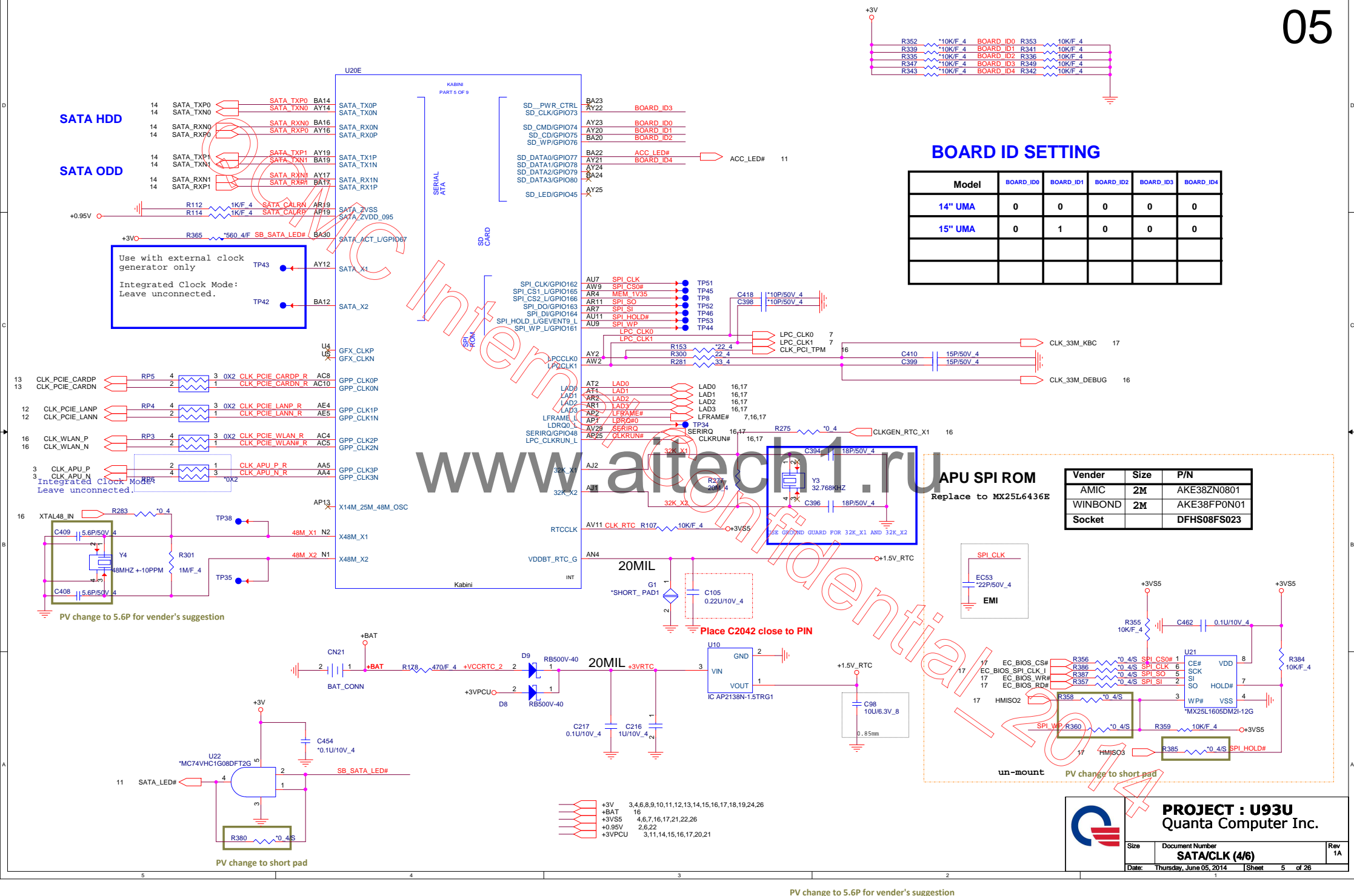
5/10 : Follow AMD suggestion to stuff R2077 for G3->S5 clock issue

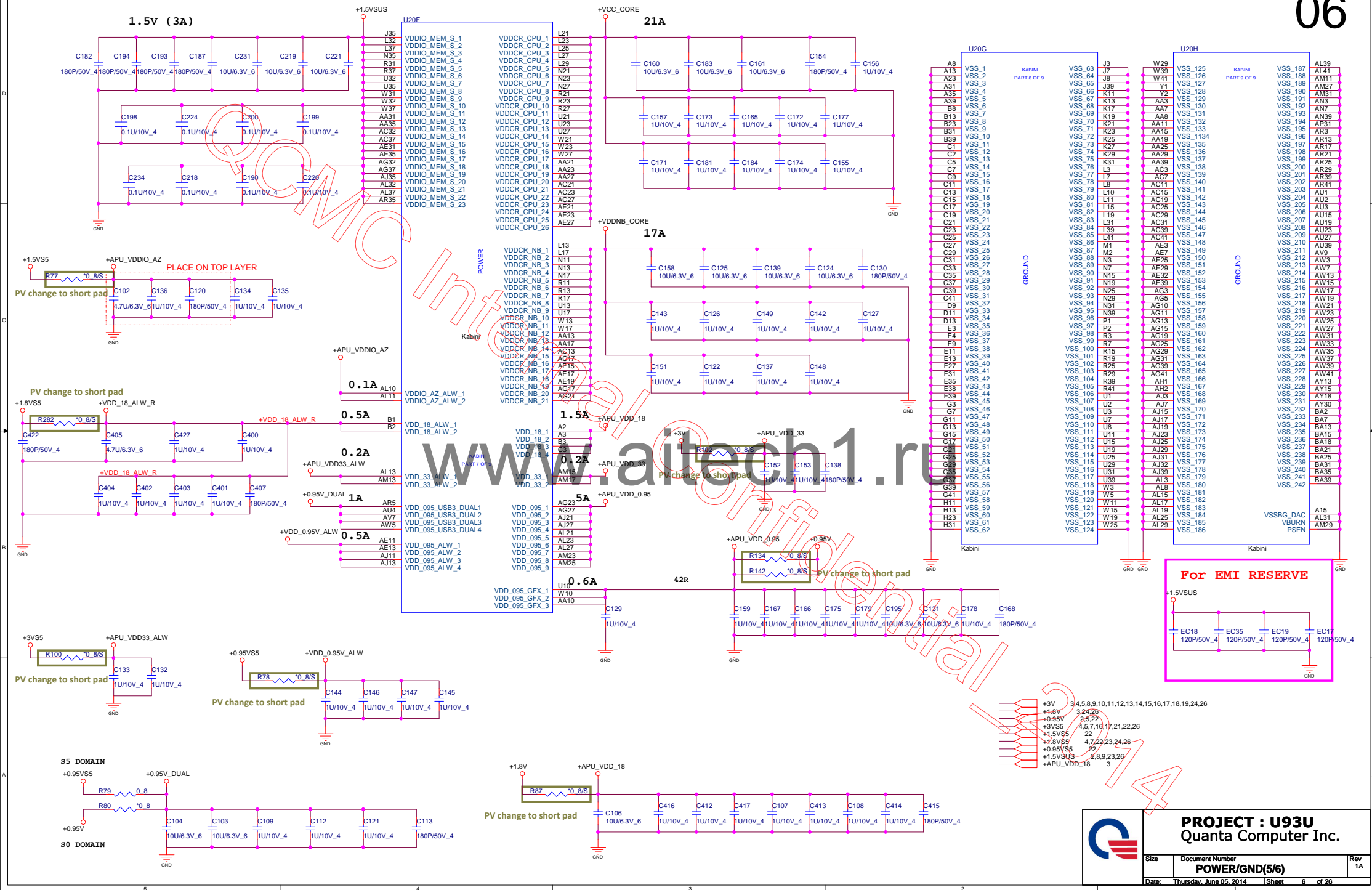
TEST2	TEST1	TEST0	Description
0	0	0	FCH TAP accessible from APU when TAPEN is asserted FCH JTAG pins are overloaded for multiple functions, in this configuration the FCH JTAG are used as non-JTAG pins
0	0	1	Reserved
0	1	X	Reserved
1	TMS	0	FCH JTAG multi-function pins are configured as JTAG pins, in this configuration the FCH TAP can be accessed from FCH JTAG pins
1	TMS	1	Use on ATE only Yuba JTAG enabled



- +3V 3,5,6,8,9,10,11,12,13,14,15,16,17,18,19,24,26
- +3V55 5,6,7,16,17,21,22,26
- +1.8V55 6,7,22,23,24,26
- +0.95V_DUAL 6



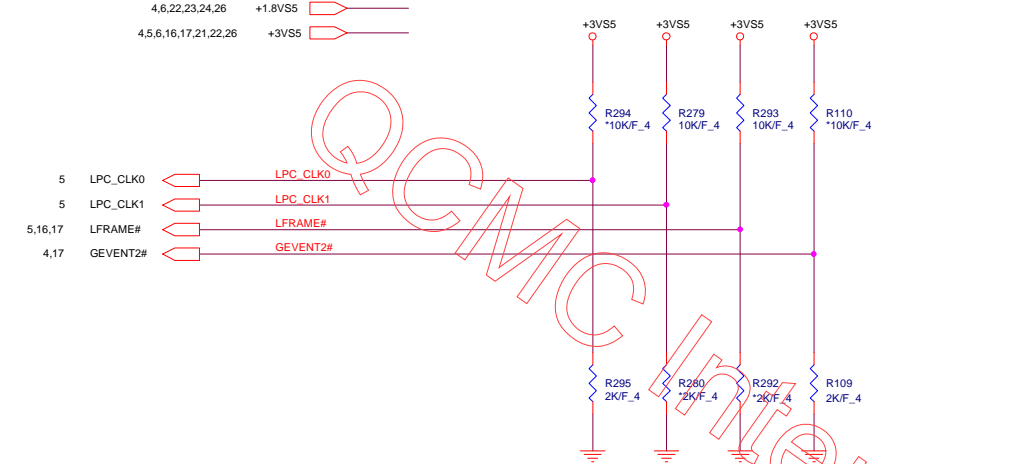




STRAPS PINS

OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

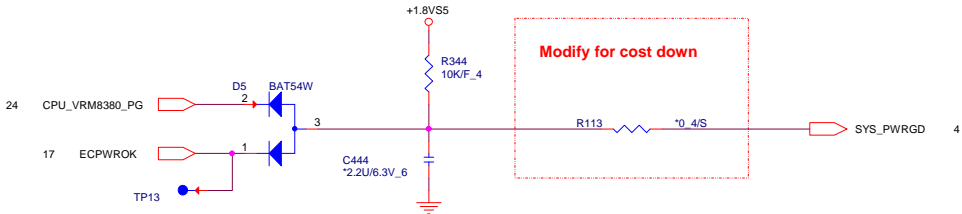
DEBUG STRAPS



REQUIRED STRAPS

					LPC_CLK0	LPC_CLK1	LFRAME#	GEVENT2#
PULL HIGH					BOOT FAIL TIMER ENABLED	CLKGEN ENABLED DEFAULT	SPI ROM DEFAULT	1.8V SPI ROM
PULL LOW					BOOT FAIL TIMER DISABLED DEFAULT	CLKGEN DISABLED	LPC ROM	3.3V SPI ROM DEFAULT

SYS_PWRGD

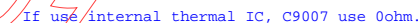
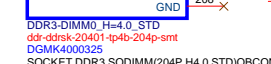


PROJECT : U93U
Quanta Computer Inc.

Size	Document Number	Rev
	STRAP (6/6)	1A
Date:	Thursday, June 05, 2014	Sheet 7 of 26



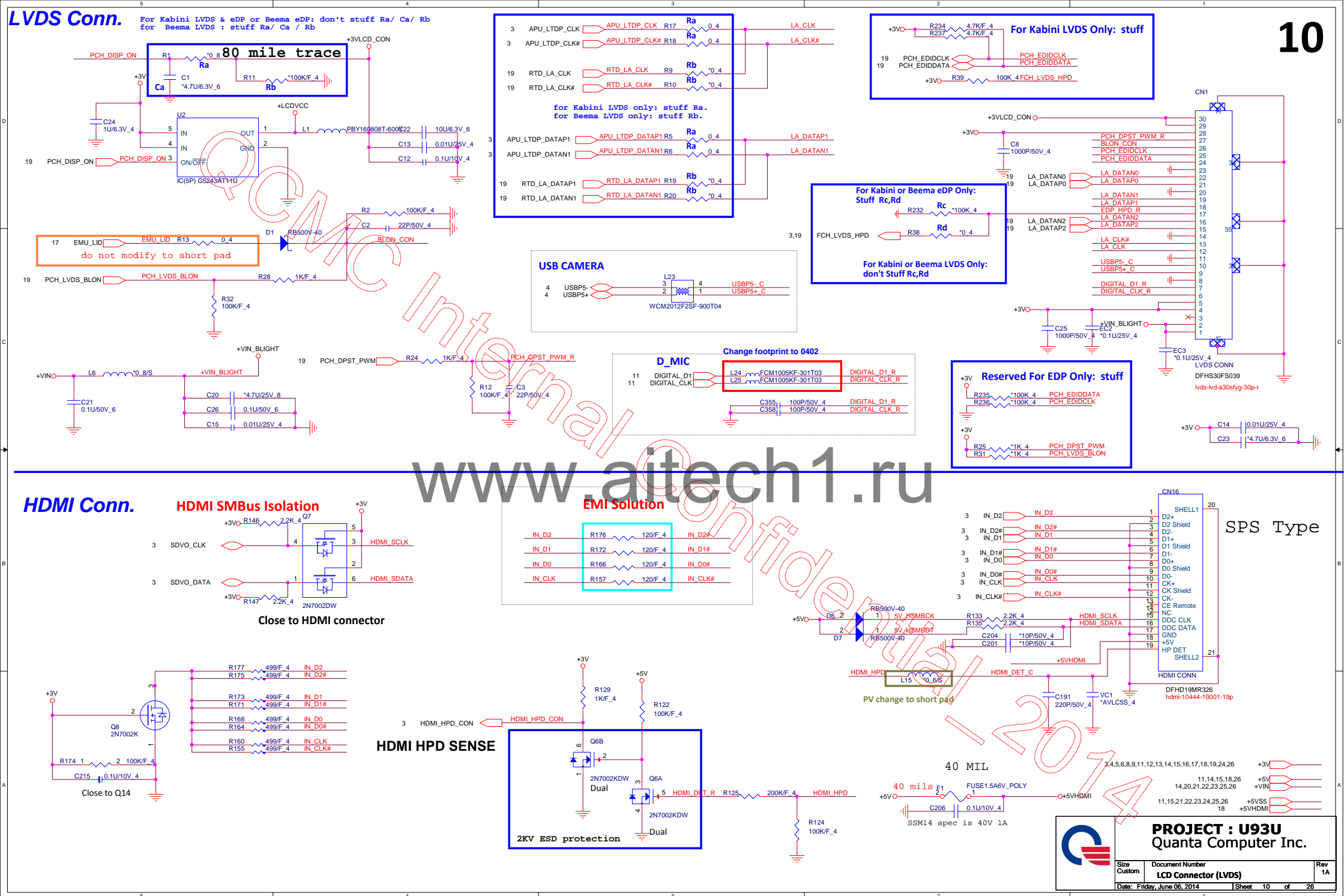
Size Custom	Document Number System Memory 1/2 (5.2H)	Rev 1A
Date: Thursday, June 05, 2014	Sheet	8 of 26



Main:AL001412003 EMC1412-1-ACZL-TR(98h)
2nd:AL000431014 TMP431ADGKR(98h)

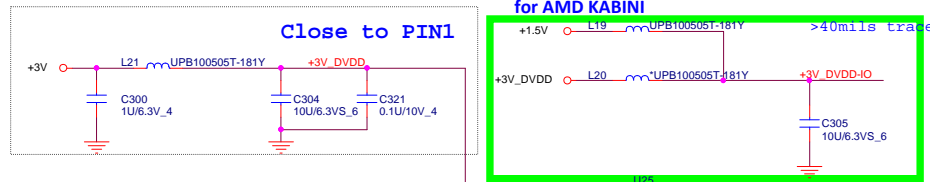


Size Custom	Document Number System Memory 2/2 (9.2H)	Rev 1A
Date: Thursday, June 05, 2014		Sheet 9 of 26



Close to PIN1

for AMD KABINI



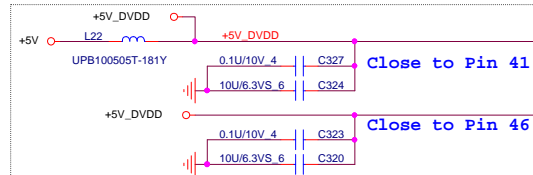
TO Digital MIC



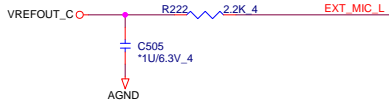
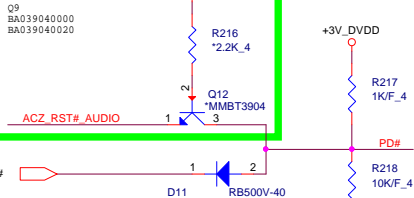
Close to PIN7

Close to Pin 9

Close to Pin 34,35,36



for AMD Kabini



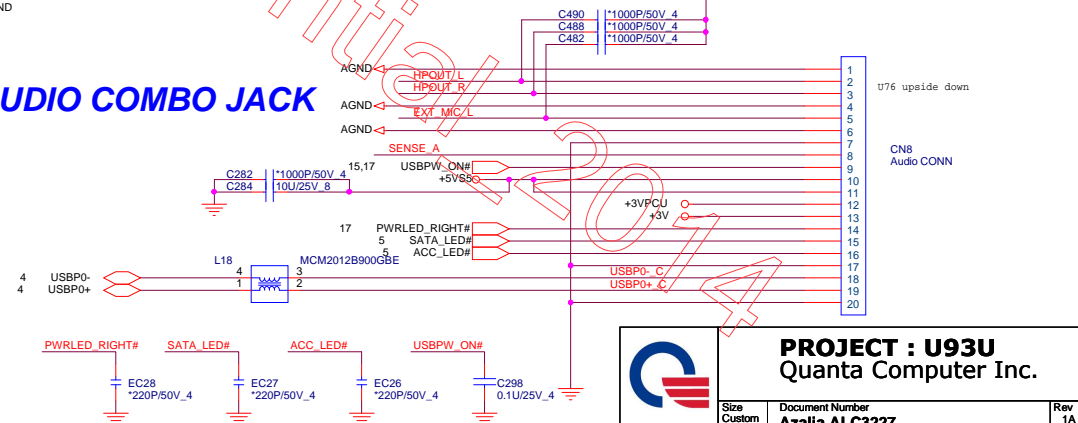
ACZ_SDIN0	EC55	*33P/50V_4
BIT_CLK_AUDIO	EC59	33P/50V_4
ACZ_SDOUT_AUDIO	EC58	*10P/50V_4
ACZ_SYNC_AUDIO	EC57	*10P/50V_4

FOR EMI

Analog

Digital

USB 2.0 AND AUDIO COMBO JACK

Check layout
mount location

TO Audio Jack MIC

TO Headphone jack

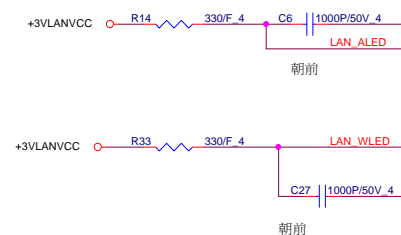
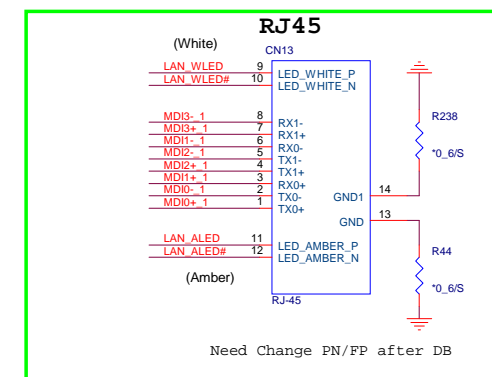
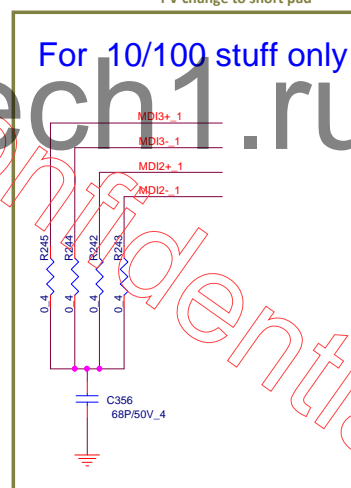
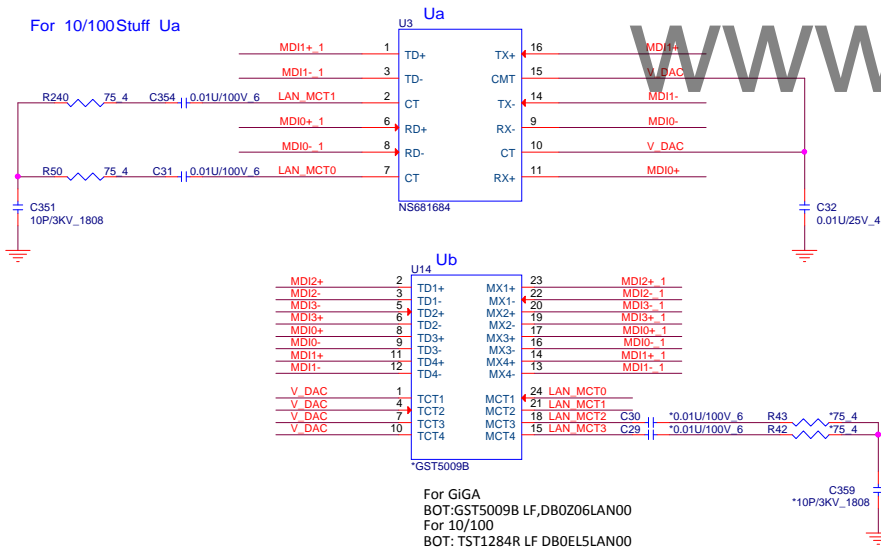
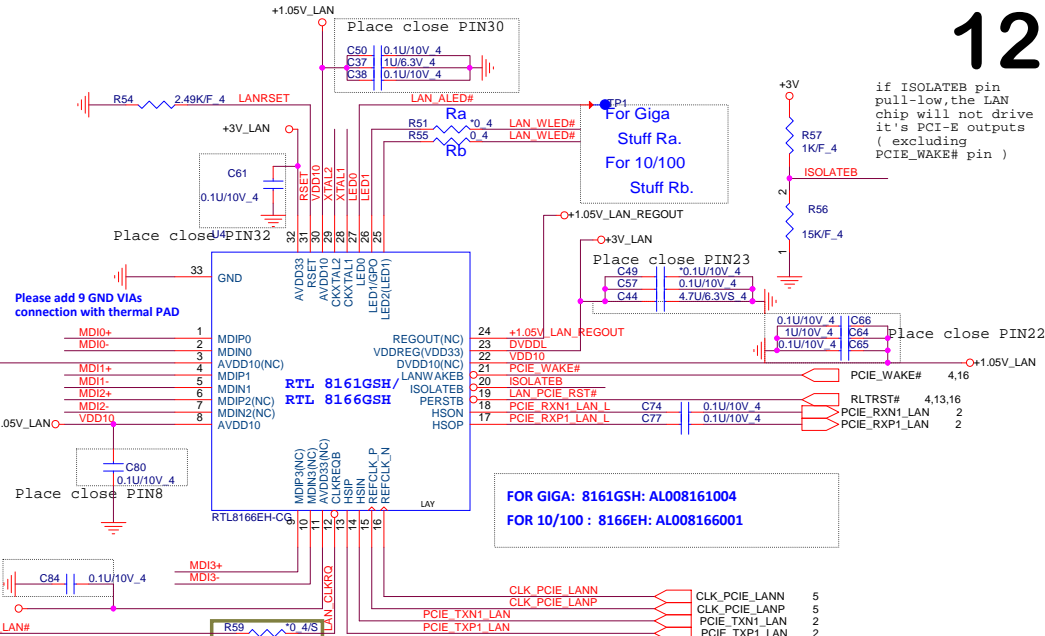
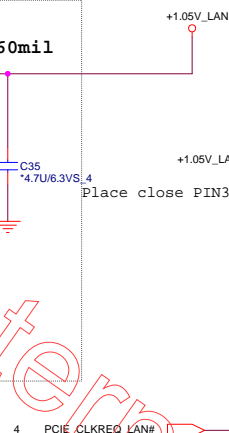
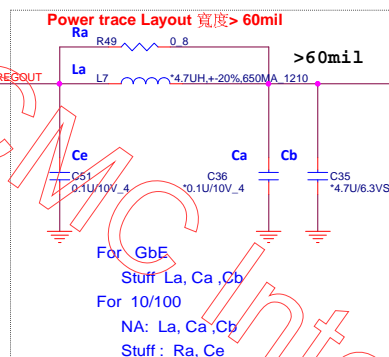
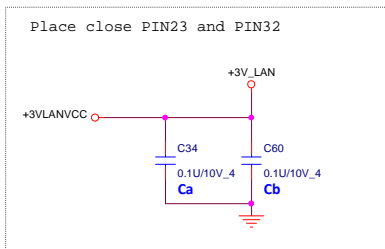
Close to CODEC
Speaker 4 ohm: 40milsKeep L_SPK+/- and R_SPK+/-
trace width 30 mil least

EMI Request Change FOOTPRINT to 0603

TO Internal Speakers

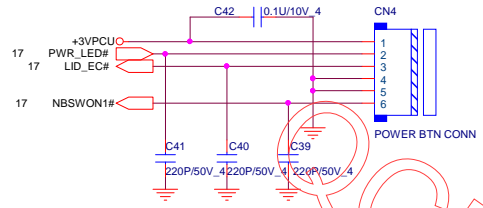
Close to CODEC
place to near U37 or under U37.PROJECT : U93U
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Azalia ALC3227	1A
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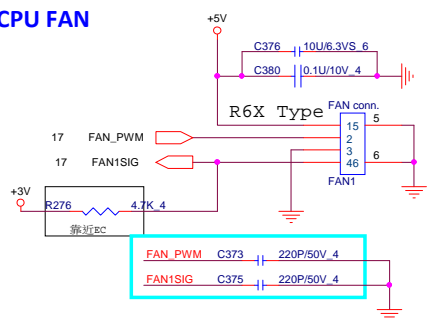


Power Button Connector

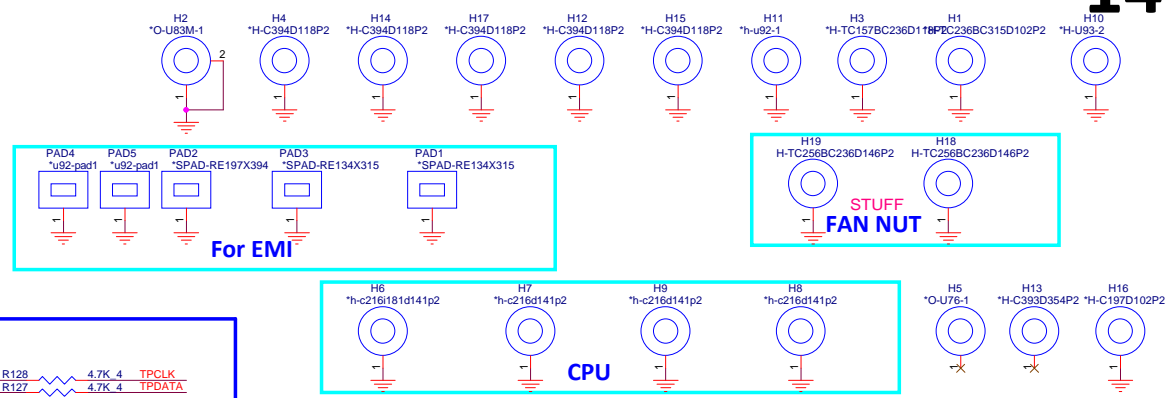
Pin1 : +3VPCU(LIDSWITCH PWR)
Pin2 : POWER LED
Pin3 : LIDSWITCH
Pin4 : GND
Pin5 : GND
Pin6 : POWERON#



CPU FAN

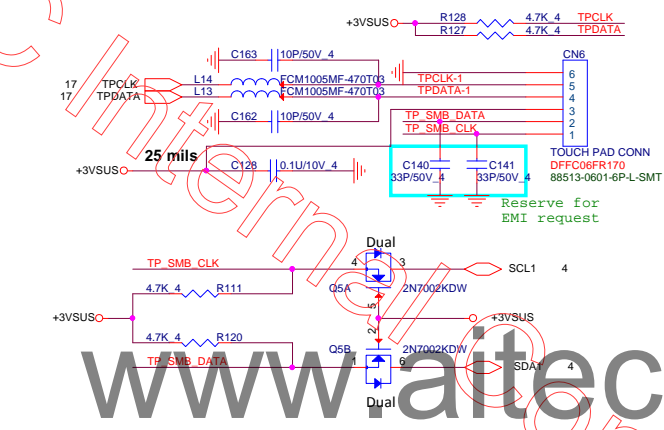
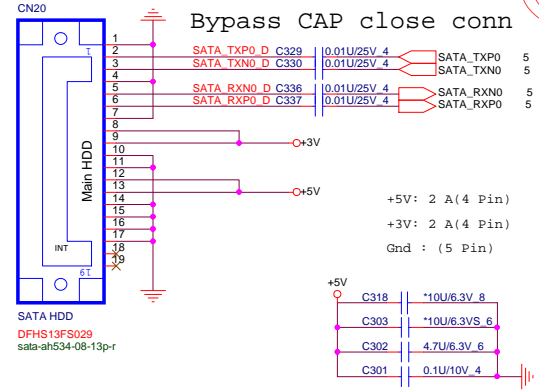


Hole

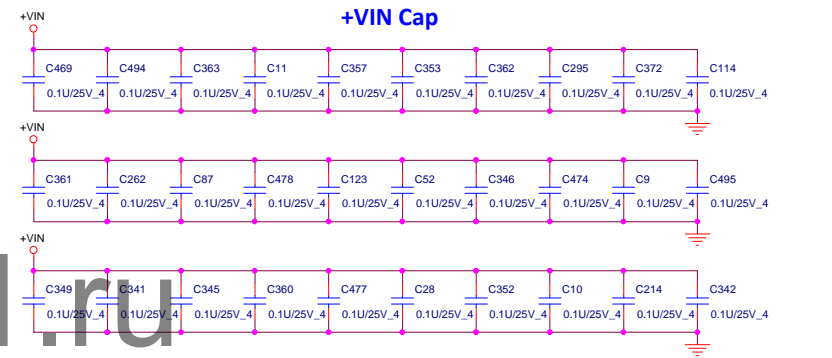


SATA HDD Connector(Cable type)

Bypass CAP close conn

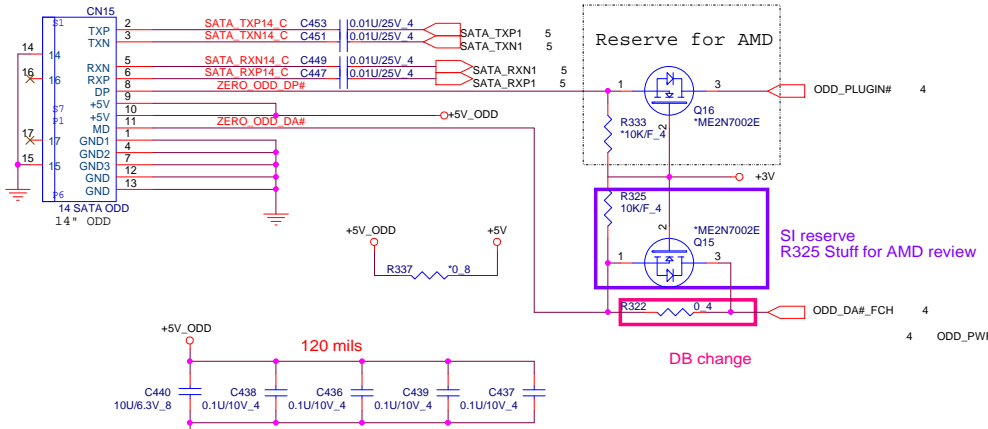


+VIN Cap



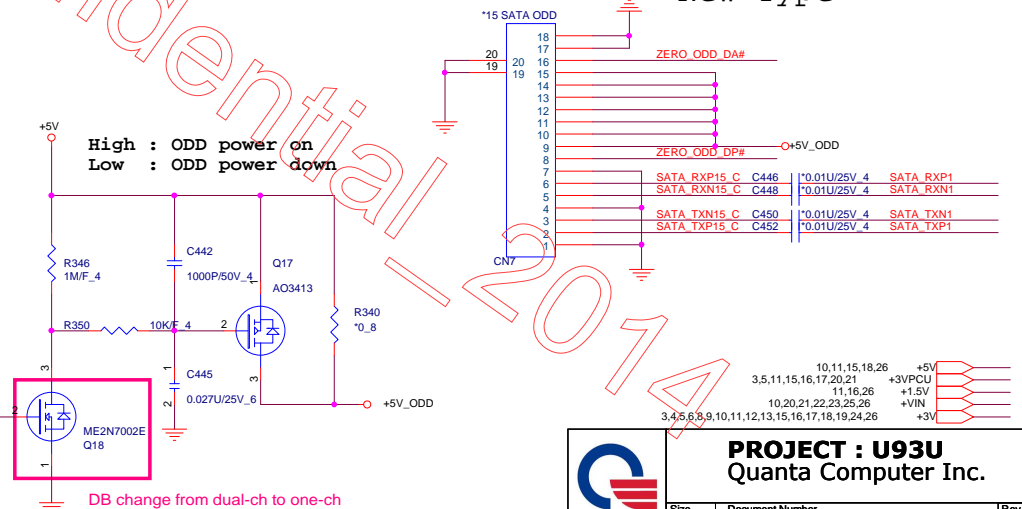
SATA ODD CONNECTOR

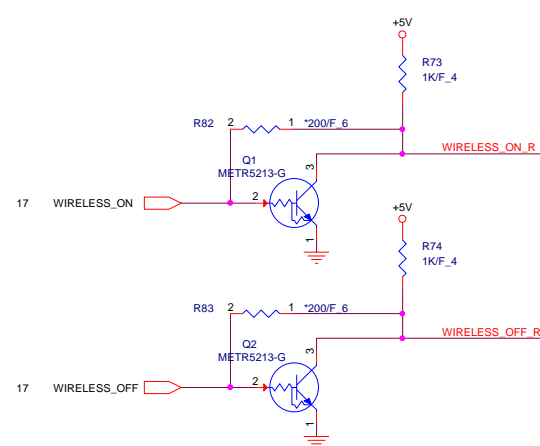
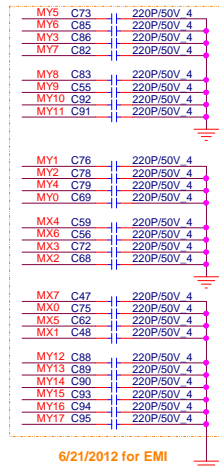
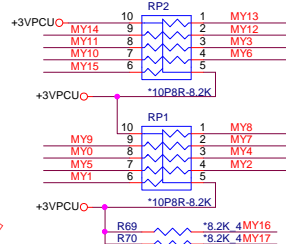
14" SATA ODD Bypass CAP close conn



15" SATA ODD

New Type

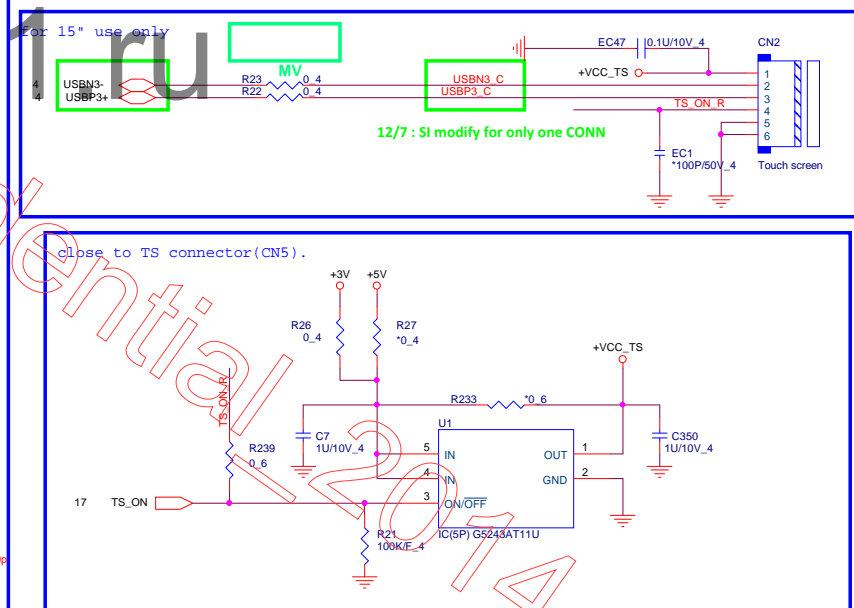
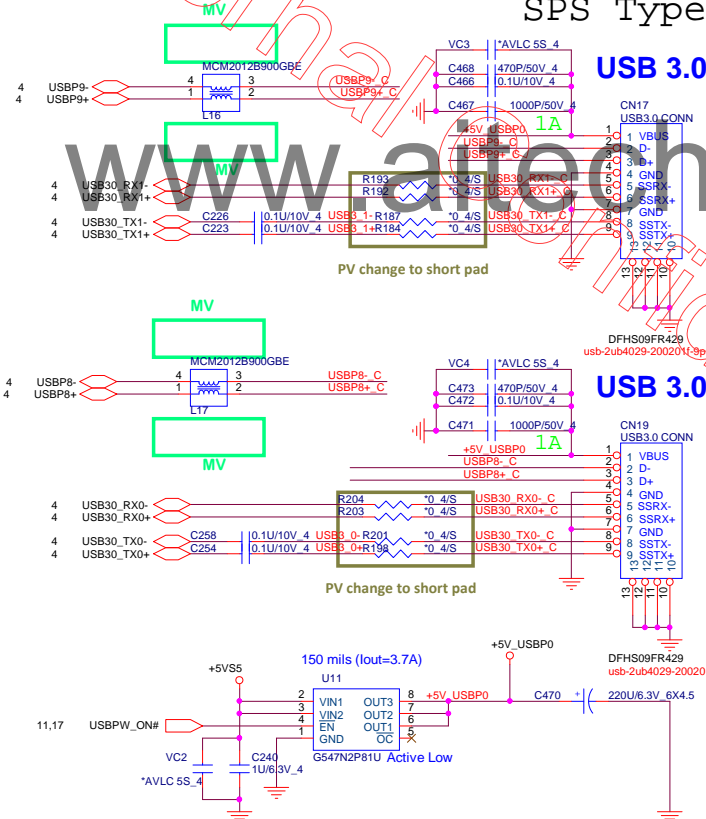




Need Change PN/FP after DB

NM9 Type

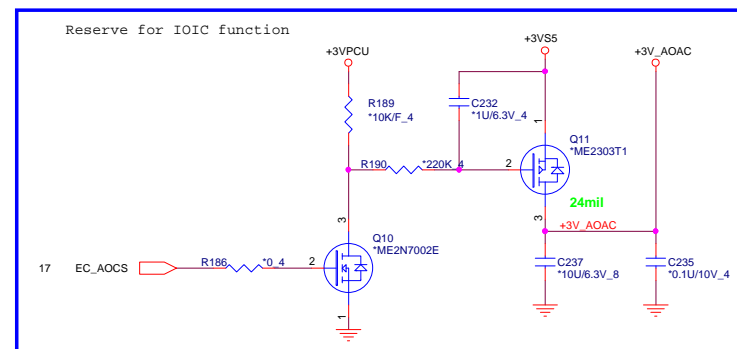
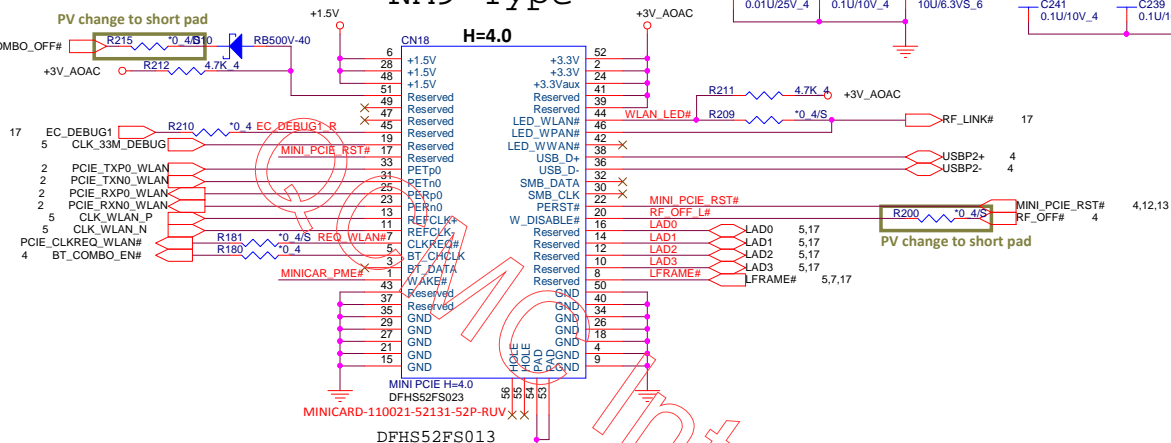
SPS Type



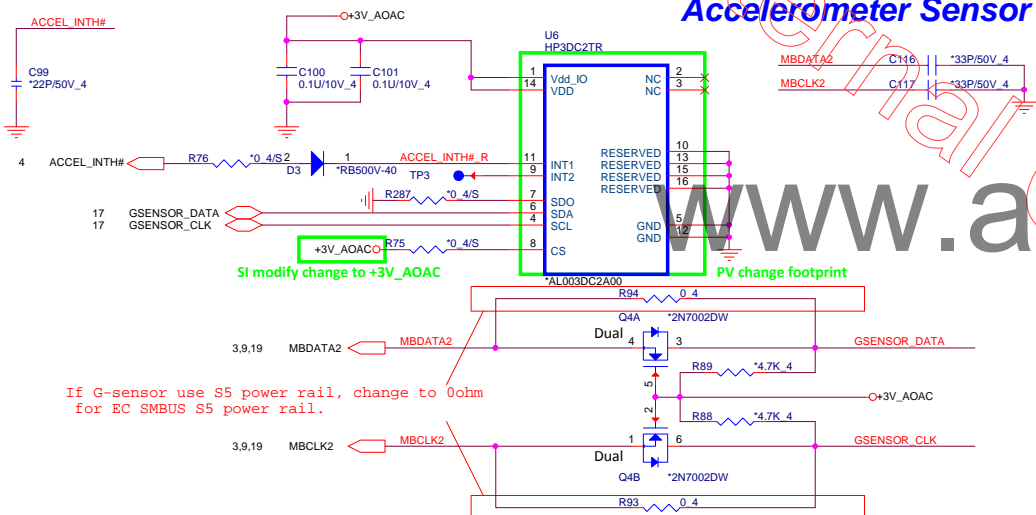
PROJECT : U93U
Quanta Computer Inc.

Size Custom	Document Number USB 3.0/KB/Green CLK	Rev 1A
Date: Thursday, June 05, 2014	Sheet 15 of	26

NM9 Type

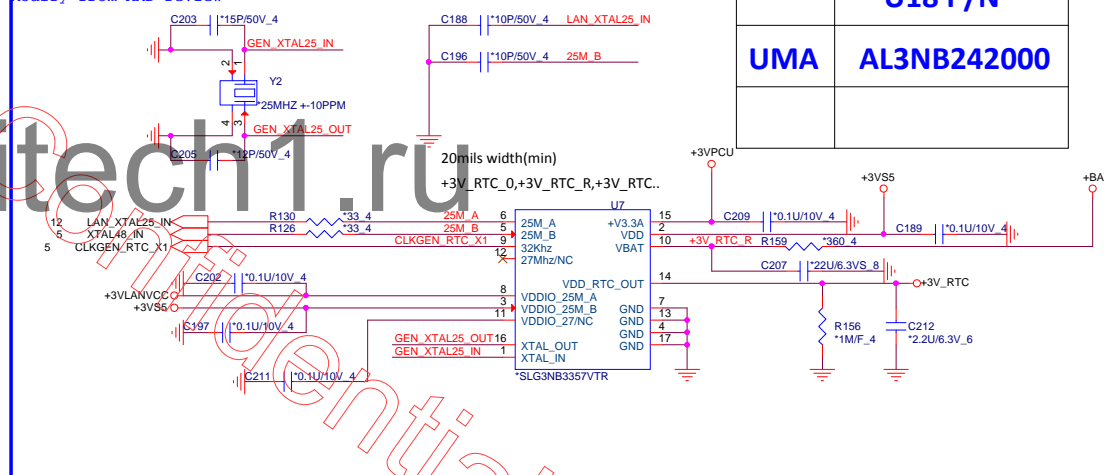


Accelerometer Sensor



Green CLK Circuitry

Modify from AMD review



U18 P/N

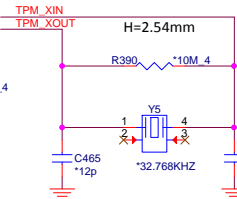
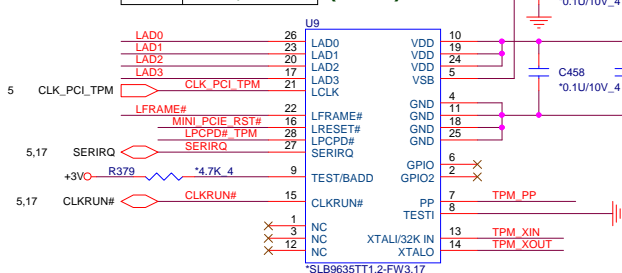
UMA

AL3NB242000

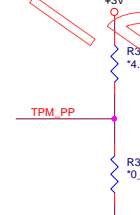
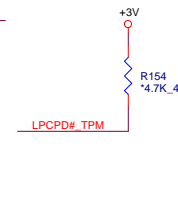
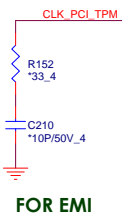
TPM (1.2)

Address

	BADD
HIGH	4EH/4F (default)



FOR EMI



+3V	3,4,5,6,8,9,10,11,12,13,14,15,17,18,19,24,26
+5V	10,11,14,15,18,26
+BAT	5
+1.5V	11,26
+3VS5	4,5,6,7,17,21,22,26
+3VPCU	3,5,11,14,15,17,20,21
+3VLAVCC	12,26



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Size Custom	Document Number MINI-PCIE/LED	Rev 1.
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PV change to short pad



Change to 1SS355 as Current loss

D4
1SS355

+3VPCU

R98 10K/F_4

R99 100F_4

AD_ID

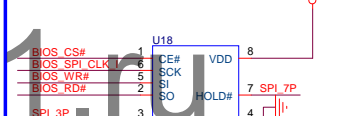
R97 12K/F_4

C118 0.1U/10V_4

C119 100P/50V_4

4M SPI EC ROM +3VPCU

Pin 18 of U18 (BIOS CS#) is connected to SPI 3P (pin 3) and SPI 7P (pin 7).



+3VPCU — R310 — 10K/F 4 — GPIO42 — R311 — 10K/F 4 —

Hi ==> A6-5200 25W CPU + DIS
Low ==>UMA/DIS/SG

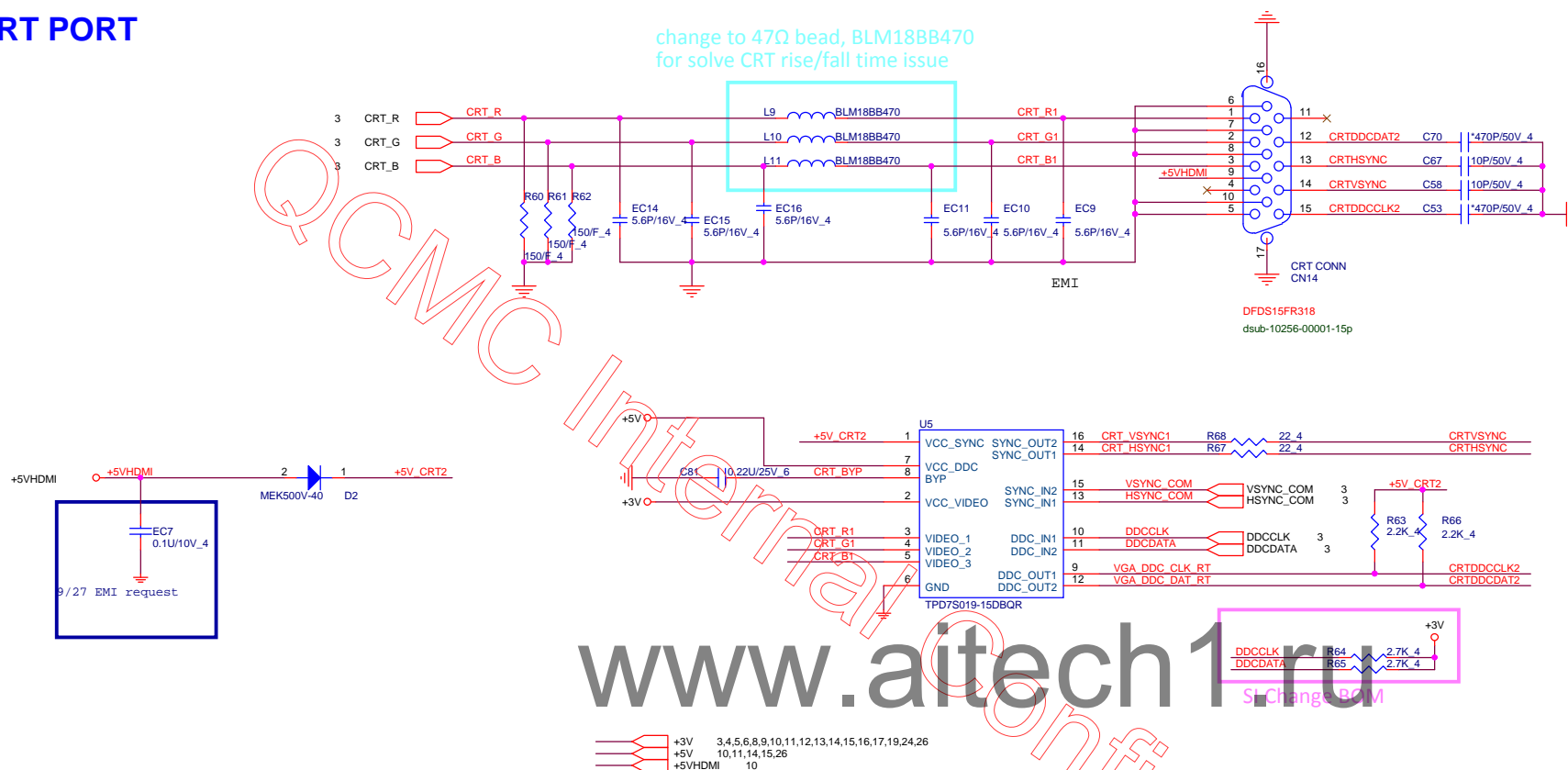
Platform model	GPIO42	adapter
A6 25W CPU + DIS	High	90W
DIS/SG/UMA	Low	65W



PROJECT : U93U
Quanta Computer Inc.

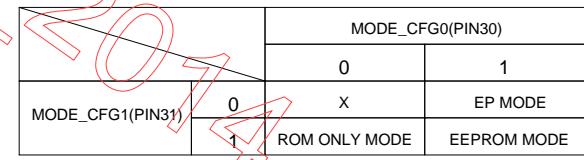
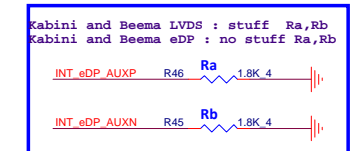
Size Custom	Document Number EC (IT8528E)/ROM	Revision 1A
Date: Thursday, June 05, 2014		Sheet 17 of 26


CRT PORT

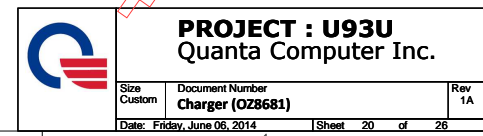


PROJECT : U93U
Quanta Computer Inc.

Size	Document Number	Rev
Custom	CRT	1A
Date: Friday, June 06, 2014 Sheet 18 of 26		

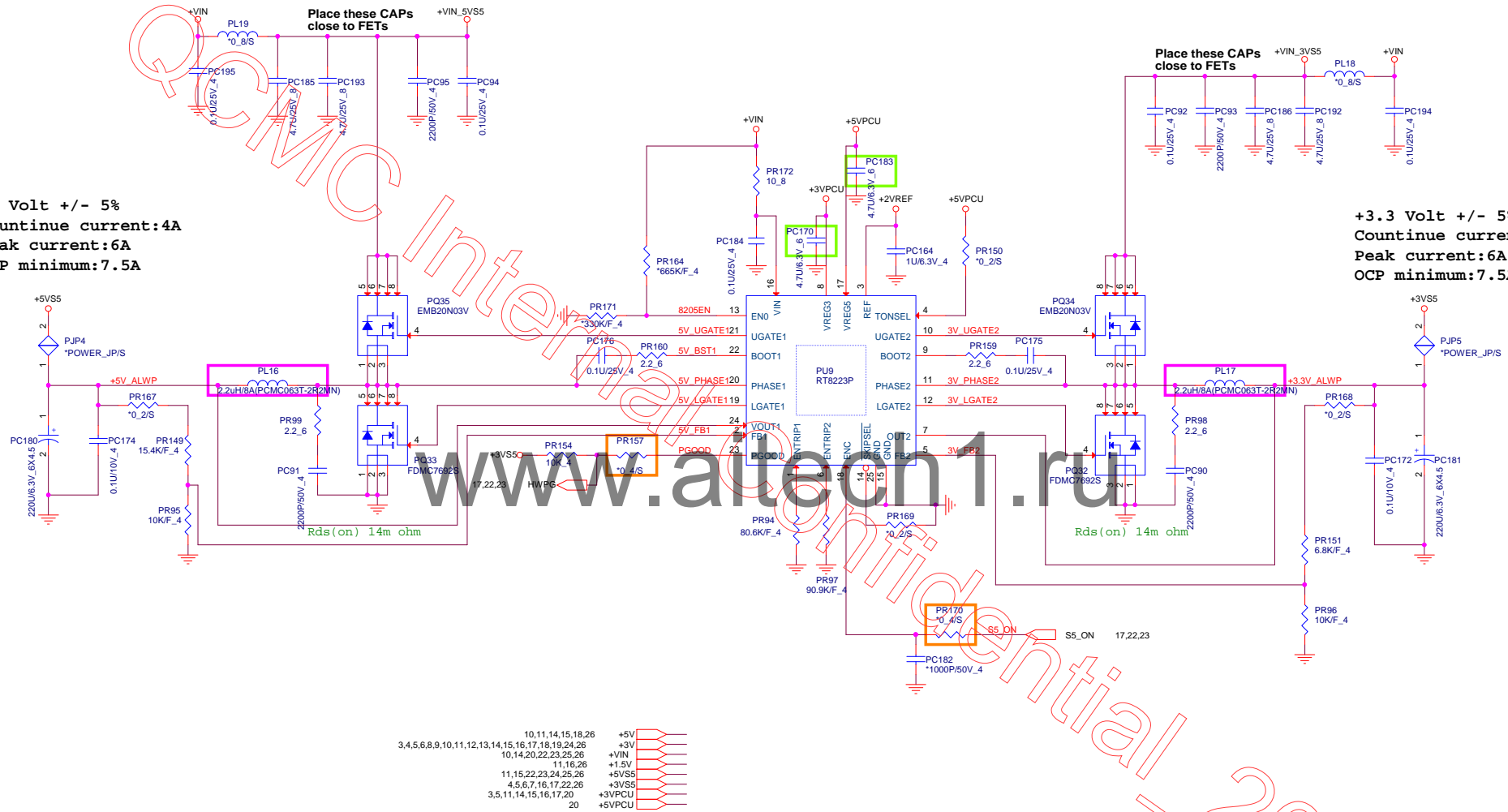


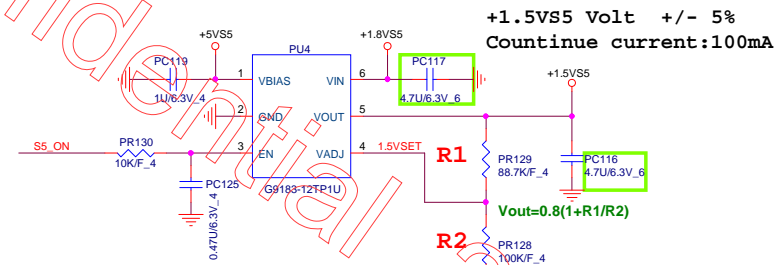
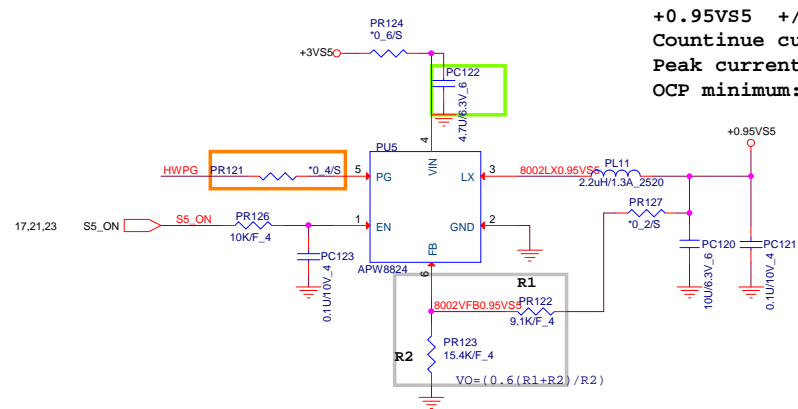
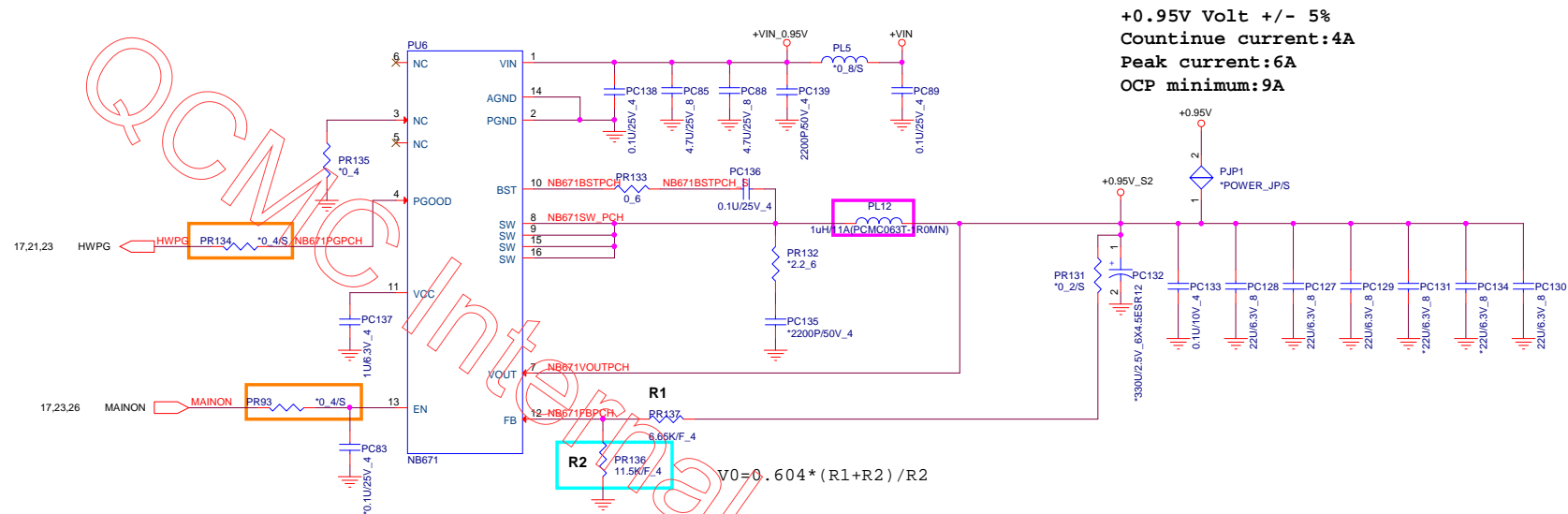
	PROJECT : U93U Quanta Computer Inc.	
	Size Custom Document Number RTD21325	Rev 3A
Date: Friday, June 06, 2014		Sheet 19 of 26

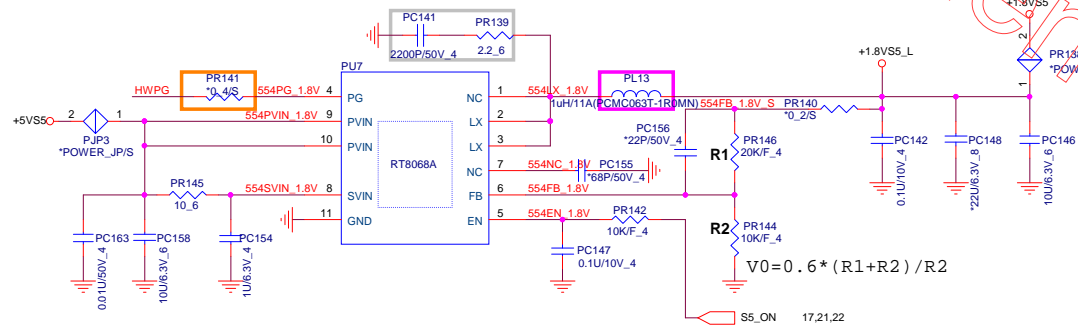
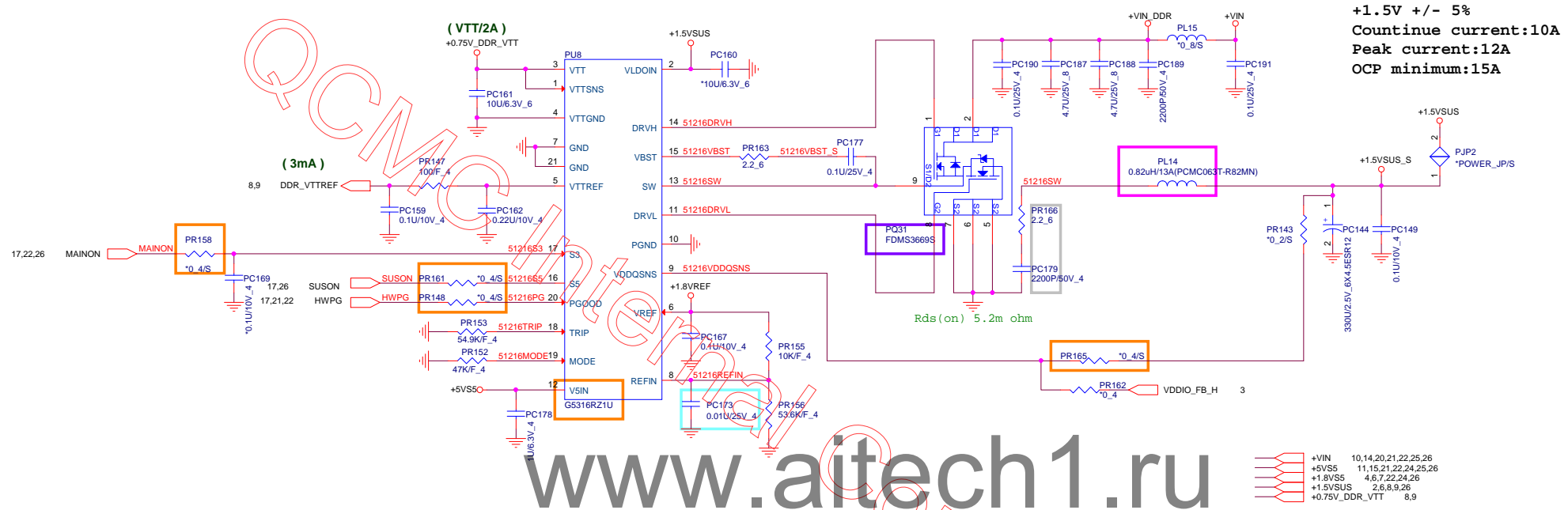


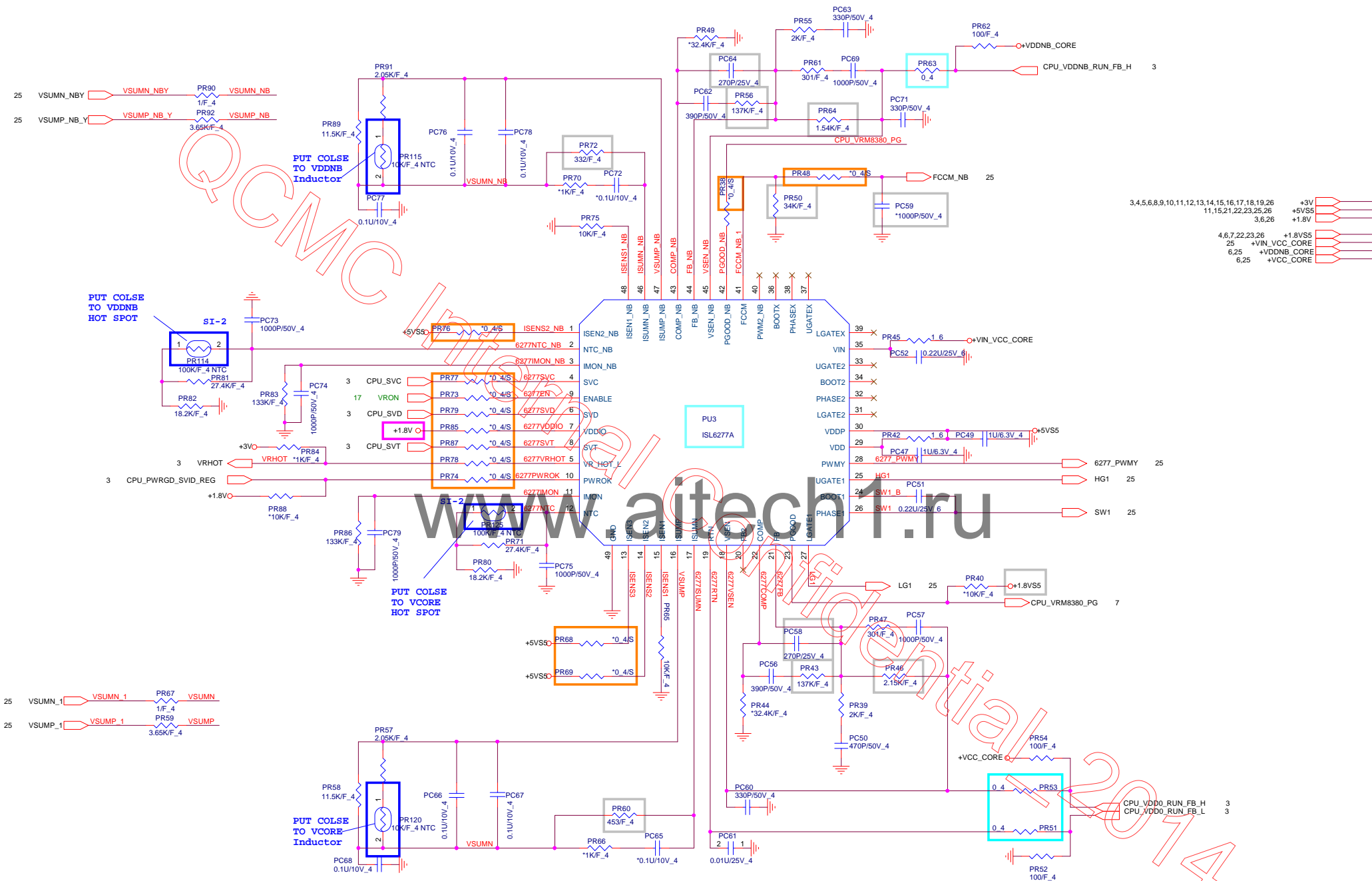
+5 Volt +/- 5%
Countinue current:4A
Peak current:6A
OCP minimum:7.5A

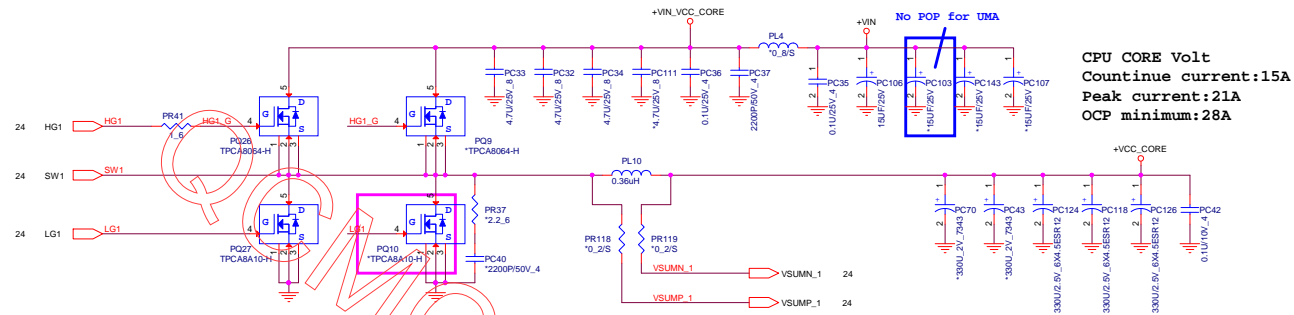
+3.3 Volt +/- 5%
Countinue current:4A
Peak current:6A
OCP minimum:7.5A











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